

Docket No.: 43876-157

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
	:	
Craig HANSEN, et al.	:	Confirmation Number: To be assigned
	:	
Serial No.: Unknown	:	Group Art Unit: To be assigned
	:	
Continuation of	:	
Application No. 10/646,787	:	
Filed August 25, 2003	:	
	:	
Filed: January 15, 2004	:	Examiner: To be assigned

For: **PROGRAMMABLE PROCESSOR AND METHOD FOR PARTITIONED GROUP  
ELEMENT SELECTION OPERATION**

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Mail Stop NEW APPLICATION  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

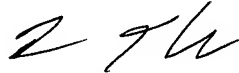
Continuation of  
Application No. 10/646,787  
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The references were cited by or submitted to the U.S. Patent and Trademark Office in parent application in Serial No. 10/646,787, filed August 25, 2003, which is relied upon for an earlier filing date under 35 USC 120. thus copies of these references are not attached. 37 CFR 1.98(d)

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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WDC99 864126-1.043876.0157

<b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>				ATTY. DOCKET NO. <b>43876-157</b>		SERIAL NO. <b>Continuation of S rial No. 10/646,787</b>	
<b>(PTO-1449)</b>				APPLICANT <b>Craig HANSEN, et al.</b>			
				FILING DATE <b>January 15, 2004</b>		GROUP <b>To be assigned</b>	
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
		US 4,785,393	11/15/1988	Chu et al.			
		US 4,814,976	03/21/1989	Craig C. Hansen, et al.			
		US 5,031,135	07/09/1991	Patel			
		US 5,280,598	01/1994	Osaki et al.			
		US 5,481,686	01/02/1996	Dockser			
		US 5,487,024	01/1996	Girardeau Jr.			
		US 5,600,814	02/1997	Gahan et al.			
		US 5,740,093	04/14/1998	Sharangpani			
		US 5,742,840	04/21/1998	Hansen et al.			
		US 5,768,546	06/1998	Kwon			
		US 5,898,849	04/27/1999	Tran			
		US 5,996,057	11/30/1999	Hunter L. Scales, III, et al.			
		US 6,041,404	03/21/2000	Patrice Roussel, et al.			
		US 6,052,769	04/18/2000	Thomas R. Huff, et al			
		US 6,173,393 B1	01/09/2001	Salvador Palanca, et al.			
		US 6,275,834 B1	08/14/2001	Derrick Chu Lin, et al			
		US 6,295,599	09/2001	Hansen et al.			
<b>FOREIGN PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes-Number *-Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
		IEEE Draft Standard for "Scalable Coherent Interface-Low-Voltage Differential Signal Specifications And Packet Encoding", IEEE Standards Department, P1596.3/D0.15 (March 1992)					
		IEEE Draft Standard for "High-Bandwidth Memory Interface Based on SCI Signaling Technology (RamLink)", IEEE Standards Department, Draft 1.25 IEEE P1596.4-199X (May 1995)					
		IBM, "The PowerPC Architecture: A Specification For A New Family of Risc Processors", 2nd Ed., Morgan Kaufmann Publishers, Inc., (1994).					
		Hewlett-Packard Co., "PA-RISC 1.1 Architecture and Instruction Set", Manual Part No. 09740-90039, (1990).					
		MIPS Computer Systems, Inc., "MIPS R4000 User's Manual", Mfg. Part No. M8-00040, (1990).					
EXAMINER				DATE CONSIDERED			

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

WDC99 864125-1.043876.0157